

December 11th, 2025

MEMTECH Launches Ecosystem Enablement Platforms to Accelerate Memory Subsystem Integration, Verification, and Prototyping

News Highlights:

- *MEMTECH today announced the availability of its Ecosystem Enablement Platforms—**Turnkey MemSoC, Memverify, ProtoDRAM-FPGA, and MemorySim-V**—built to accelerate SoC memory integration, verification, virtual prototyping, and FPGA bring-up. The platform suite helps engineering teams reduce risk, shorten design cycles, and improve predictability from architecture to pre-silicon validation. These platforms are available now through MEMTECH's Early Access and Customer Engagement Program for qualified customers.*

Cupertino, California — California Memory Technologies Inc. (**MEMTECH**), an American semiconductor company headquartered in Cupertino, today announced the availability of its **Ecosystem Enablement Platforms**—a four-part platform suite designed to help SoC teams **integrate, verify, prototype, and validate memory subsystems faster and with lower risk.**

The platform suite includes: **Turnkey MemSoC** (SoC memory integration), **Memverify** (verification acceleration), **ProtoDRAM-FPGA** (FPGA bring-up), and **MemorySim-V** (virtual prototyping). Together, they provide an end-to-end workflow to shorten time-to-silicon and reduce memory integration uncertainty across AI/ML, automotive, industrial, and IoT markets.

MEMTECH Ecosystem Enablement Platforms

1) Turnkey MemSoC — A Turnkey SoC Memory Integration Platform

Key Features

- Rapid subsystem integration templates and reference configurations
- Automated collateral packaging: integration guides, configuration notes, and interface documentation
- Integration support for subsystem bring-up planning and SoC readiness

Target Markets

- AI/ML edge SoCs and vision processors
- Networking/WiFi/Broadband SoCs
- Application processors, storage controllers, and multimedia SoCs

- Automotive/industrial SoCs requiring predictable integration

Problem Solved

- Reduces integration effort and risk by enabling faster, repeatable memory subsystem insertion into complex SoCs.

2) Memverify — A Verification Acceleration Platform

Key Features

- Verification acceleration methodology for faster coverage closure
- Pre-built test and validation workflows for subsystem-level verification
- Debug and analysis support to speed subsystem sign-off

Target Markets

- SoC design & verification teams across consumer, industrial, and automotive
- Teams adopting new memory subsystems under tight schedules
- Organizations seeking reproducible verification flows

Problem Solved

- Tackles slow verification cycles and late-stage debug by accelerating subsystem validation and improving confidence earlier in the design cycle.

3) ProtoDRAM-FPGA — An FPGA Bring-Up Platform

Key Features

- FPGA-based prototyping flow for subsystem evaluation and pre-silicon validation
- Early bring-up readiness: interface validation, configuration exercise, and throughput checks
- Enables hands-on system experimentation before silicon availability

Target Markets

- AI/edge compute and storage controller teams
- System companies validating software + hardware co-design early
- Teams needing early proof points for performance and integration readiness

Problem Solved

- Eliminates “first-silicon surprises” by enabling realistic bring-up and integration trials before tape-out.

4) MemorySim-V — A Virtual Prototyping Platform

Key Features

- Virtual subsystem modeling for architectural exploration and performance estimation
- Early software readiness and system-level evaluation
- Configurable models to support fast what-if studies (bandwidth, latency, power states)

Target Markets

- Architecture teams optimizing memory performance early
- Software teams needing pre-silicon development environments
- IoT and embedded teams modeling power/performance tradeoffs

Problem Solved

- Removes guesswork in memory architecture decisions by enabling earlier system-level insight into performance, latency, and power behavior.

Leadership Quote

“Great memory technology needs more than IP—it needs an ecosystem that helps engineers adopt it with speed and confidence. MEMTECH’s enablement platforms are designed to reduce integration friction, accelerate verification, and bring real-world prototyping earlier into the development cycle. This is how we help customers cut time-to-silicon while improving quality and predictability.”

— Saswat Mishra, Founder & CEO, MEMTECH

Availability

The MEMTECH Ecosystem Enablement Platforms—Turnkey MemSoC, Memverify, ProtoDRAM-FPGA, and MemorySim-V—are available now through MEMTECH’s Early Access and Customer Engagement Program.

Qualified customers can request:

- Platform overviews and datasheets
- Sample user guides and workflows
- Demonstrations and technical briefings

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